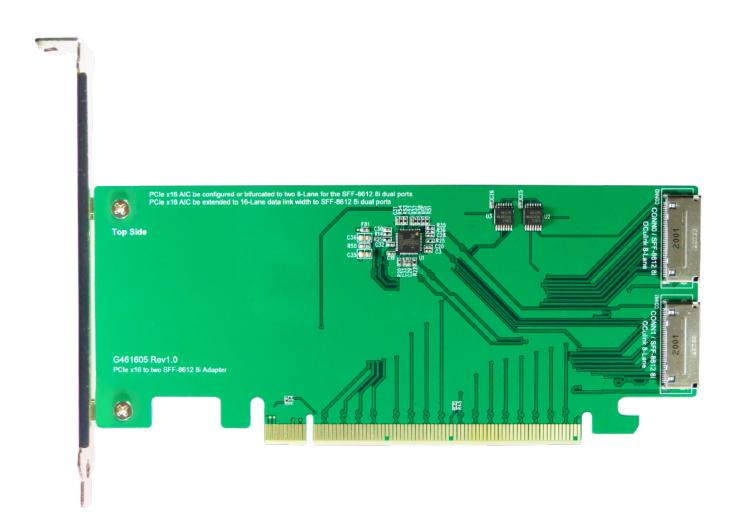


# PE1604

# PCle Gen3 16-lane to OCulink 8-Lane (SFF-8612 8i) Dual-port AIC



#### Notice:

The Mainboard with CPU and BIOS needs to support bifurcation to eusure PCIe x16 link width to bifurcate two x8 link width for four NVMe SSD. If not, PE1604 Riser Card only supports PCIe x16 link width

#### PCle Gen3 16-lane to OCulink 8-Lane (SFF-8612 8i) Dual-port AIC

#### **Features**

- PE1604 can be bifurcated in quad x4 data link width configurations (need CPU and BIOS both support)
- PE1604 can be bifurcated in two x8 data link width configurations(need CPU and BIOS both support)
- ※ PE1604 can be used over a cable to extend x16 data link width
- PE1604 features four internal NVMe(SFF-8612 8i x2) for high-perfoemace storage connectivity.
- Build in OCulink(SFF-8612) 8i connector 2 sets, pin-out defined by SFF-9402
- ※ Eight internal lanes per OCulink 8i port, 6.4Gb/s per port
- Supports four physical NVMe devices
- Supports SFF-9402 clock pin out
- Supports Intel define Mini SAS HD(SFF-8643) clock pin out
- Enables the system clock signal to be used over longer trace lengths and over a cable
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#### **Specifications**

- Compliant with NVM Express 1.2a
- Support PCI Express Base Specification Rev 3.1
- Support SSD\_Form\_Factor\_Version1\_a
- Compliant with PCI\_Express\_OCuLink\_1.0

#### Operating system support

- Linux
- Windows 8 &8.1
- Windows 10
- **WEFI 2.3.1 or later**

### Application

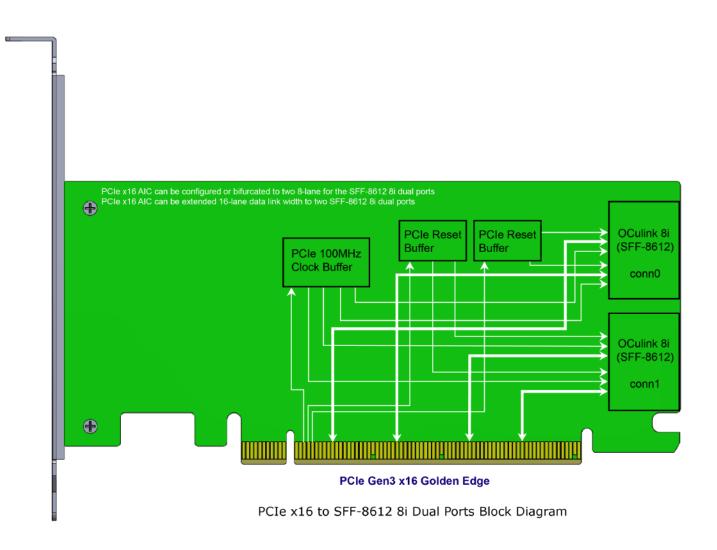
- Servers
- \* Data Center
- Storage Racks
- Storage Controller
- Servers RAIDs
  - (Redundant Array of Independent Disks)
- Switchs



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## PCIe Gen3 16-lane to OCulink 8-Lane (SFF-8612 8i) Dual-port AIC

# **Block Diagram**



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